深圳市炬烜科技有限公司 CHIP SUN TECHNOLOGY CO., LTD

APPROVAL Sheet



CUSTOMER:	Quartz 1
DESCRIPTION:	SMD5032 122.880MHz VCXO LV-PECL
MANUFACTURER PART NO.:	FVO122.880M3.3SM5P-50D
CUSTOMER PART NO:	
USED IN MODEL :	
REVISION	A1

	承	认	A	PPROVAL
工程部		品质部		采购部
TECHNOLOGY DEPT.	QUA	LITY DEF	PT.	PURCHASING DEPT.

Date: <u>June 12, 2023</u>



深圳市炬烜科技有限公司

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Rev	<u>Revise page</u>	Revise contents	Date	<u>Ref.No.</u>	Reviser
A1	ALL	Initial released	2023.06.10	N/A	DavidJiang

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1. VCXO SPECIFICATION					
1.1 Frequency:	122.880MHz				
1.2 Holder Type :	SMD5032				
1.3 Frequency Tolerance:	±50ppm Overall Temperature stability is Inclusive of all conditions: Calibration Tolerance at +25℃, frequency stability over the operating temperature range, supply voltage change, output load changes, shock, vibration, and 1st year aging at +25℃.				
1.4 Supply Voltage:	3.3V _{DC} ±10%				
1.5 Input Current :	75mA max				
1.6 Operating Temperature Range:	-40℃ To +85℃				
1.7 Storage Temperature Range:	-55℃ To +125℃				
1.8 Output Waveform:	LV-PECL				
1.9 Output Load:	50 Ω V _{DD} -2V				
1.10 Output Low Level VoL:	1.680V max				
1.11 Output High level V_{OH} :	2.275V min				
1.12 Frequency Control Range :	±100ppm min (Vc=1.65±1.65V)				
1.13 Linearity :	10% max.				
1.14 Duty Cycle :	45~55% (at 50% V _{DC})				
1.15 Rise& Fall Time :	1.0nS max/ 20%~80% output swing level				
1.16 Start-up Time :	10mS max				
1.17 RMS Phase Jitter:	1.5 pS max. (12KHz~20MHz)				
1.18 Phase Noise :	-85dBc/Hz Typ. (100Hz offset)				
	-110dBc/Hz Typ. (1KHz offset)				
	-115dBc/Hz Typ. (10KHz offset)				
	-120dBc/Hz Typ. (100KHz offset)				
1.19 Insulation resistance:	500M Ω (DC100±10V)min				
1.20 Aging:	±3ppm/Year max.				
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1.21 Output Waveform :

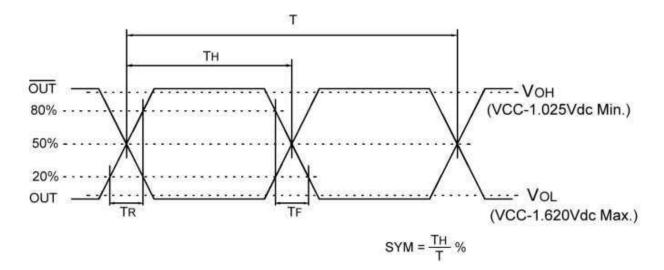
Refer to fig.1

Standard atmospheric conditions

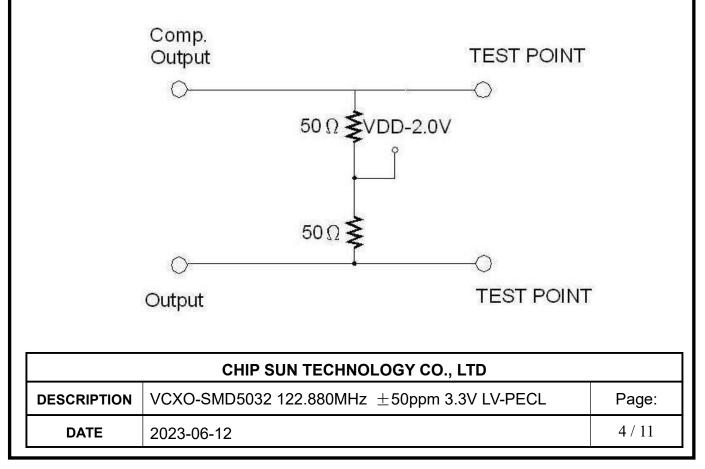
Unless otherwise specified, the standard range of atmospheric conditions for making measurement and tests are as follow:

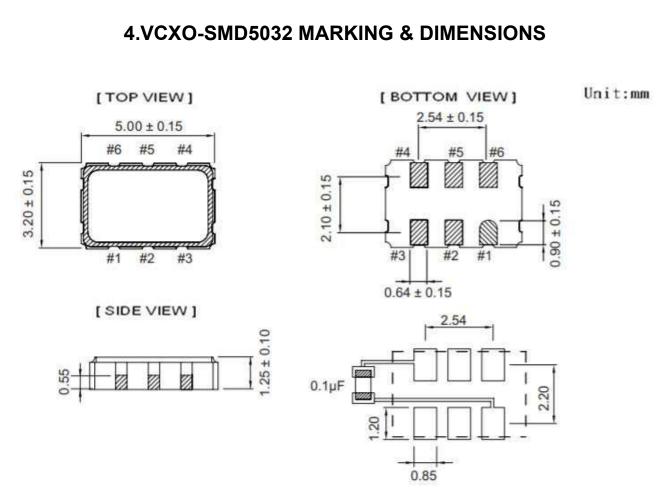
Ambient temperature : 25±3°C Relative humidity : 40%~70%

2. Output Waveform



3. Test circuit





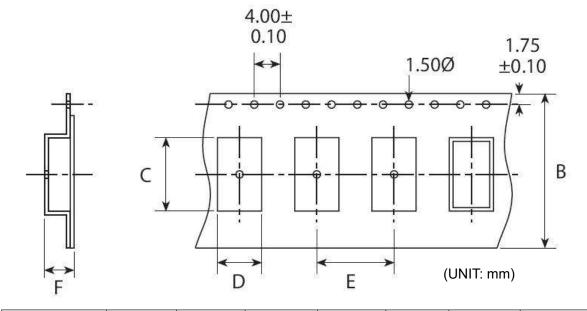
To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vdd and GND pads.

Pin	Function
#1	Vcon
#2	Tri-State
#3	GND
#4	Output
#5	Comp. Output
#6	Vdd

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5. VCXO-SMD5032 EMBOSS CARRIER TAPE & REEL

a.) Dimensions of Carrier Tape

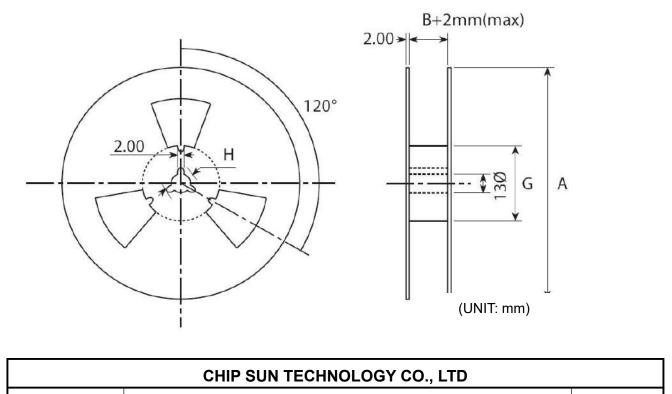


	А	В	С	D	Е	F	G
SMD5032	180±2.0	12.0±0.2	5.40 ± 0.10	3.60 ± 0.10	8.0 ± 0.1	1.4 ± 0.1	60.5 ± 1.0

b.) Dimensions of Reel

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c.) Storage condition

Temperature: +40deg.C Max. Relative Humidity: 80% Max.

d.) Standard packing quantity

1,000PCS / REEL

e.) Material of the tape

Таре	Material
Carrier tape	A – PET
Top tape	Polyester

f.) Label contents

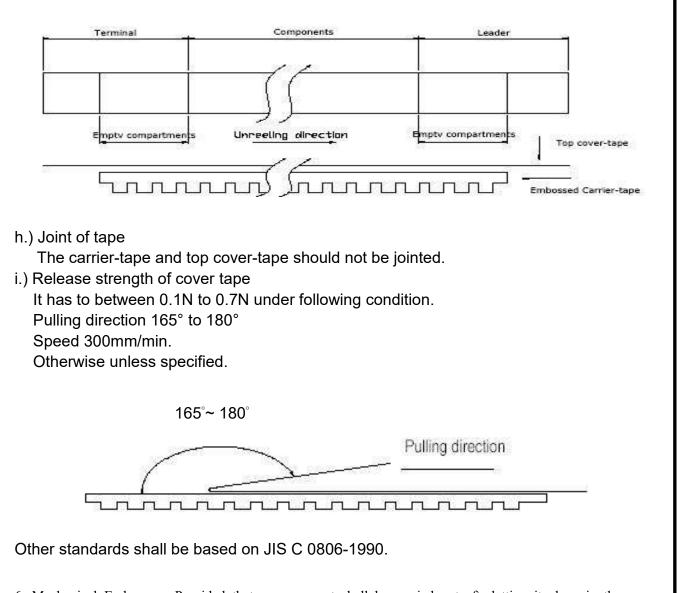
.The type of product	
.Our specification No.	PART NUMBER
.Your Part No.	Lot. NO:
.Lot No.	HOLDER TYPE
.Nominal Frequency	FREQUENCY
.Quantity	REMAKS
.Our Company Name	QUANTITY
	CHIP SUN TECHNOLOGY CO., LTD

Sticks label for every reel.

g.) Taping dimension

Leader	Cover-tape	The length of cover-tape in the leader is more than 400 mm including empty embossed area.	
	Carrier-tape	After all products were packaged, must remain more than twenty pieces or 400 mm empty area, which should be sealed by cover-tape.	
Terminal	Cover-tape	The tip of cover-tape shall be fixed temporary by paper tape and roll around the core of reel one round.	
Terminai	Carrier-tape	The empty embossed area which are sealed by top cover-tape must remain more the 40 mm.	

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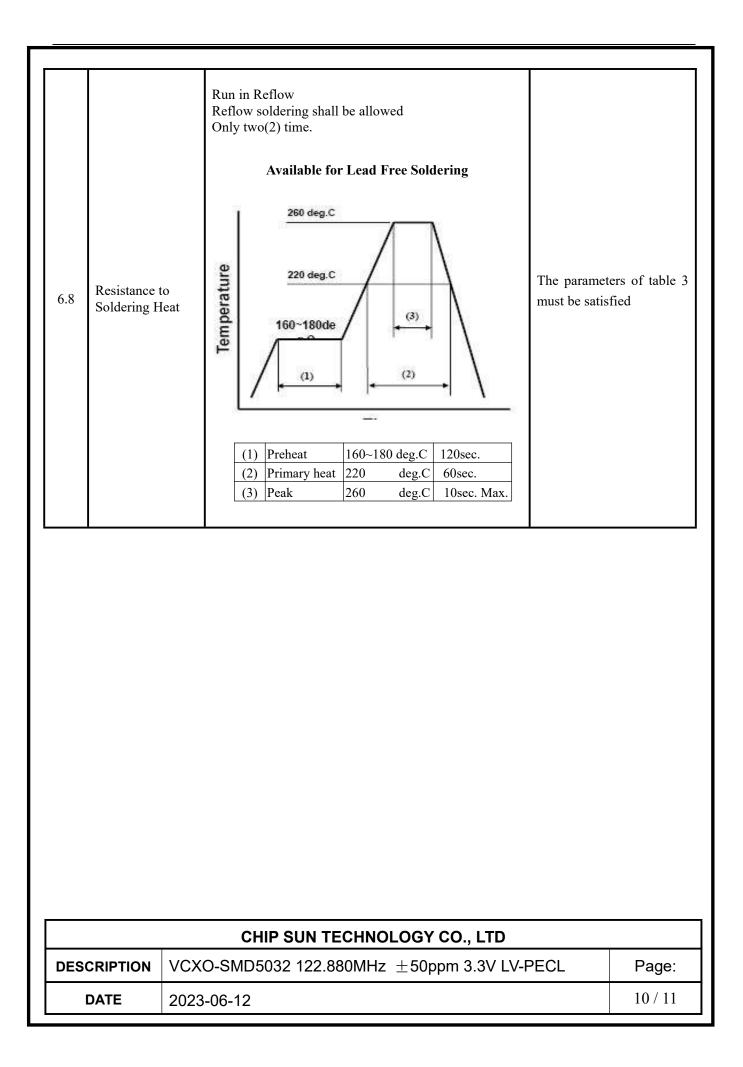


6. Mechanical Endurance: Provided that measurement shall be carried out afterletting it alone in the room

temperature for 1 hour.

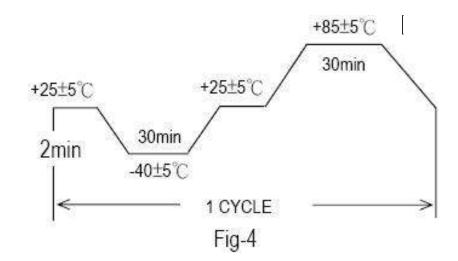
	Item	Conditions	Specifi	cations	
6.1	Drop	Should be satisfied after dropping three times from the height of 100 cm onto hard wooden board of thickness more than 30mm.	The parameters of table 3 must be satisfied		
6.2	Vibratio	n Should be satisfied after supplying following (1)Vibration Frequency: 10~55Hz (2)Cycle: 1 to 2 Min. (3)Full Cycle: 0.8mm P-P. (4)Direction: X.Y.Z (5)Time: 2 Hours / Each Direction	The parameters of table 3 must be satisfied		
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DESCRIPTION VCXO-SME		VCXO-SMD5032 122.880MHz \pm 50ppm 3.3V LV-F	MD5032 122.880MHz \pm 50ppm 3.3V LV-PECL		
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6.3	Substra Bendin	Apply the following pressure must be sati te Direction: see Fig –1	ters of table 3 sfied		
6.4	Adhesio	Apply the following pressure must be sati	ters of table 3 sfied		
6.5	Body strengt	Apply the following pressure must be sati Direction: see Fig –3	ters of table 3 sfied		
	R 230 R 230 Pressure jg R 230 R 230 R 252 R 2				
6.6	Fig- Seal	Fig-2 Fig- Less than 2.0 x 10-9 Pa.m3/sec by Helium leak detector. Also, no serial bubble is observed by Fluorinate tests.	<u>.</u>		
6.7	Solder ab	ility $3 \text{ sec Dip in } 235^{\circ} \mathbb{C} \pm 5^{\circ} \mathbb{C} \text{ solder.}$ (Use ROSIN type flux for solder.) More than 9 shall be cover solder.	0% of lead ered by new		
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7. Environmental Endurance: Provided that measurement shall be carried out afterletting it alone in the room temperature for 1 hour.

	ltem	Conditions	Specifications
7.1	Humidity	Should be satisfied after letting it alone at $+60^{\circ}C\pm 2^{\circ}C$ in humidity of 90%~95% for 500 hours.	The parameters of table 1 must be satisfied. No physical damage.
7.2	Storage in Low Temperature	Should be satisfied after letting it alone at -40°C±2°C for 500 hours.	The parameters of table 1 must be satisfied. No physical damage.
7.3	Storage in High Temperature	Should be satisfied after letting it alone at +85°C±2°C for 500 hours.	The parameters of table 1 must be satisfied. No physical damage.
7.4	Temperature Cycle	Should be satisfied after supplying the following temperature cycle (100 cycles). (Refer to Fig-4). Temperature shift from low to high, high to low shall be done in 1°C/min.	The parameters of table 1 must be satisfied. No physical damage.



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